

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**PATENT APPLICATION**

Applicants : David Lewis et al.  
Application No. : 10/766,464 Confirmation No.: 3178  
Filed : January 27, 2004  
For : ERROR CORRECTION FOR PROGRAMMABLE  
LOGIC INTEGRATED CIRCUITS  
Art Unit : 2114  
Examiner : Timothy M. Bonura

New York, New York 10036  
July 23, 2007

Mail Stop AF  
Hon. Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

REPLY UNDER 37 C.F.R.  
§ 1.116 TO OFFICE ACTION

Sir:

In reply to the final Office Action dated May 30, 2007, applicants hereby propose the following amendments of the above-identified patent application:

**Amendments of the Claims (proposed)** begin on page 2 of this Reply Under 37 C.F.R. § 1.116 to Office Action.

**Remarks** begin on page 13 of this Reply Under 37 C.F.R. § 1.116 to Office Action.